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Identification

Data segment grower datmk_
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Purpose

Datmk_ is used in the implementation of PL/I static storage to "grow" storage regions as needed. Normally it is called only through an out-reference in a linkage section which specifies it in the "call-before-linking" option.

<u>Usage</u>

Use of datmk is specified in EPLBSA by:

segref datmk_,datmk_

segref segment,symbol(datmk_(arglist))

arglist dec size

dec initialswitch

arg initializer

Here <u>segment</u> and <u>symbol</u> are the names of a segment and an in-reference in that segment's linkage section. At execution time, the first reference to <u>symbol</u>, e.g. the instruction

eapbp symbol

causes a trap to the linker, which in turn calls datmk_. If <u>segment</u> is not active in the process, datmk_ creates it and its linkage section. Then if <u>symbol</u> is not listed as an in-reference in <u>segment's</u> linkage section, datmk_ grows <u>segment</u> by <u>size</u> words and creates the in-reference pointing to the newly-grown storage.

If <u>initialswitch</u> is non-zero, datmk_ fills in the faulting link pair and calls the user's initializing procedure located at <u>initializer</u>. This call has the form of a call to a PL/I internal procedure (see BP.3.00 for details)

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with no arguments. Since this call does not go through the linkage section, if the initializing routine uses the base pair 1b ← 1p it must obtain the proper values itself. Assuming that $1b \leftarrow 1p$ is properly set, however, the initializing routine may freely refer to symbol.

Finally datmk_ returns to the linker, which uses the RCU instruction to restart the user's program at the faulting instruction. By the time this instruction has finished executing, the data region has been grown and initialized, and the instruction has had its proper effect.

<u>Implementation</u>

Datmk_ is called by the linker as follows:

call datmk_ (argpointer, panelpointer);

where <u>argpointer</u> is a pointer to the user's argument list specified in the segref pseudo-op, and panel is a pointer to stored machine conditions as follows:

> words 0-7 SCU information

> > 8-15 base registers

16-23 arithmetic registers