

To MTB Distribution
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Subject: M264 MOS Memory Board/edit_mos_rscr Test

INTRODUCTION

This MTB describes the testing done with the edit_mos_rscr program and the new 16 pin, 16K, MOS Memory RAM chip printed wiring board (M264).

DESCRIPTION

A M264 memory module (512K words), consisting of 8 M264 printed wiring boards and 2 memory controller boards, was installed on System M as Memory D, store A on February the 1st. The memory was exercised on Multics service for 5 days; one error was detected and logged on February 5th.

The main goals of the test were:

1. To determine if M264 memory modules operated correctly on the Multics system.
2. To determine if edit_mos_rscr would call out the correct chip location, given an EDAC error address.

The edit_mos_rscr program is called by both poll_mos_memory and HEALS to convert memory addresses into the correct physical board and chip location.

Several RAM chips were removed from the socketed M264 board (at different times) and poll_mos_memory was run, each time detecting the correct chip. The next time that HEALS was run however, the log listed a ZAC code and a syndrome code. Subsequent running of HEALS did call out the correct chip locations and it is felt that somehow HEALS had initialized the old version of edit_mos_rscr during the initial invocation. A RAM chip location chart for the M264 PWB is attached for those interested in physical chip layout.

